

INPUT SPECIFICATION		Options
1. Frequency range:	720MHz IF B/W: $\pm 40\text{MHz}$, $\pm 200\text{MHz}$	(see product table) (other values possible)
2. Connector:	SMA	
3. Impedance:	50 Ω	
4. Return loss:	$\geq 15\text{dB}$	

OUTPUT SPECIFICATION	
5. Frequency range:	Any 1GHz or 2GHz slot within 19 to 31GHz (see product table)
6. Connector:	K-type
7. Impedance:	50 Ω
8. Return loss:	$\geq 18\text{dB}$
9. 1dB compression point:	+10dBm
10. Third order intercept::	+20dBm

TRANSFER CHARACTERISTICS													
11. Gain:	0 to 30dB, adjustable in 0.1dB steps												
12. Gain ripple:	<table style="width: 100%; border: none;"> <tr> <td style="width: 40%;">over $\pm 200\text{MHz}$:</td> <td style="width: 40%;">$\leq 1.5\text{dB p.t.p.}$</td> <td style="width: 20%;"></td> </tr> <tr> <td>over $\pm 40\text{MHz}$:</td> <td>$\leq 0.8\text{dB p.t.p.}$</td> <td></td> </tr> <tr> <td>over output band, 1GHz:</td> <td>$\leq 3\text{dB p.t.p.}$</td> <td style="text-align: right;">(1)</td> </tr> <tr> <td>over output band, 2GHz:</td> <td>$\leq 4\text{dB p.t.p.}$</td> <td style="text-align: right;">(1)</td> </tr> </table>	over $\pm 200\text{MHz}$:	$\leq 1.5\text{dB p.t.p.}$		over $\pm 40\text{MHz}$:	$\leq 0.8\text{dB p.t.p.}$		over output band, 1GHz:	$\leq 3\text{dB p.t.p.}$	(1)	over output band, 2GHz:	$\leq 4\text{dB p.t.p.}$	(1)
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13. Group delay distortion:	<table style="width: 100%; border: none;"> <tr> <td style="width: 40%;">ripple, $\pm 40\text{MHz}$</td> <td style="width: 40%;">$< 3\text{ns ptp}$</td> <td style="width: 20%;"></td> </tr> <tr> <td>linear, $\pm 40\text{MHz}$</td> <td>$< 0.03\text{ns/MHz}$</td> <td></td> </tr> <tr> <td>parabolic, $\pm 40\text{MHz}$</td> <td>$< 0.01\text{ns/MHz}^2$</td> <td></td> </tr> </table>	ripple, $\pm 40\text{MHz}$	$< 3\text{ns ptp}$		linear, $\pm 40\text{MHz}$	$< 0.03\text{ns/MHz}$		parabolic, $\pm 40\text{MHz}$	$< 0.01\text{ns/MHz}^2$				
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14. Gain stability, 0°C to 50°C:	$\pm 1\text{dB}$												
24hr. at constant temperature:	$\pm 0.2\text{dB}$												
15. Frequency stability, -10°C to +60°C:	<table style="width: 100%; border: none;"> <tr> <td style="width: 40%;">10^{-7} from 0°C to +50°C</td> <td style="width: 60%;"></td> </tr> <tr> <td>10^{-8} at constant temperature over 24 hrs.</td> <td></td> </tr> </table>	10^{-7} from 0°C to +50°C		10^{-8} at constant temperature over 24 hrs.									
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16. External reference:	10MHz, 0dBm 5MHz, 0dBm												
17. Synthesiser step size:	1kHz												
18. Noise figure (full gain):	$< 20\text{dB}$												

Spurii	
19. Image rejection:	$> 50\text{dB}$
20. In-band spurii (at 0dBm output):	$< -55\text{dBc typical}$

PHASE NOISE	
21. 10Hz:	$< -48\text{dBc/Hz}$
22. 100Hz:	$< -70\text{dBc/Hz}$
23. 1kHz:	$< -78\text{dBc/Hz}$
24. 10kHz:	$< -83\text{dBc/Hz}$
25. 100kHz:	$< -93\text{dBc/Hz}$
26. 1MHz:	$< -110\text{dBc/Hz}$
27. Mains related:	$< -60\text{dBc}$

MISCELLANEOUS					
28. Power supply:	115V/230V $\pm 10\%$ 50/60Hz $\pm 10\%$, $> 60\text{VA}$				
29. Mechanical:	1U 19" frame, 400, 500 or 520mm deep (depends on model)				
30. Temperature:	<table style="width: 100%; border: none;"> <tr> <td style="width: 40%;">Operating:</td> <td style="width: 60%;">0° to 50°C</td> </tr> <tr> <td>Storage:</td> <td>-40° to 85°C</td> </tr> </table>	Operating:	0° to 50°C	Storage:	-40° to 85°C
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31. Relative humidity:	<table style="width: 100%; border: none;"> <tr> <td style="width: 40%;">Operating:</td> <td style="width: 60%;">0 to 90%</td> </tr> <tr> <td>Storage:</td> <td>0 to 95%</td> </tr> </table>	Operating:	0 to 90%	Storage:	0 to 95%
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Storage:	0 to 95%				
32. Summary alarm:	NO and NC dry relay contacts via rear mounted connector				
33. Summary alarm indication:	Front panel LED				
34. Remote control:	<ul style="list-style-type: none"> ● RS232 or RS422/RS485, connector D-type 9P F ● Serial emulation over TCP/IP, connector RJ45 ● SNMP and HTTP over TCP/IP Ethernet, connector RJ45 				

(1) Ripple spec measurement does not include 200MHz segment below the lowest limit and above the highest.

Model	Input (c)	Output
U2588-1	720 ± 200MHz	23.2 - 24.2GHz (b)
U2588-2	720 ± 200MHz	22.7 - 23.7GHz (b)
U2588-3	720 ± 200MHz	25.4 - 26.4GHz (b)
U2588-4	720 ± 200MHz	22.8 - 23.8GHz (b)
U2588-5	720 ± 200MHz	25.2 - 26.2GHz (b)
U2572	720 ± 200MHz	25 - 27GHz (b)
U2552	720 ± 200MHz	29 - 31GHz (b)
U2553	720 ± 200MHz	25 - 28GHz (b)

- (a) This specification covers ALL frequency agile upconverters with 720MHz or 1.2GHz IF and RF output from 19GHz to 31GHz. **This table lists ONLY more common models.** Consult out office for other models configurations.
- (b) Output frequencies are an illustrative sample. Any other values from 19GHz to 31GHz, in 50MHz steps, are possible. RF coverage different from 1GHz or 2GHz is also possible.
- (c) Other input bandwidths possible: ±20MHz, ±40MHz, ±50MHz, ±200MHz, ±300MHz.

Option: All RF, IF and 10MHz reference connectors can be square flange with 4 screw fitting



K-type square flange



N-type square flange



BNC

NOTE

All Novella's frequency converter synthesisers are of the conventional phase-locked type. No DDS techniques or ICs are used. DDS synthesisers suffer from an inherent phase uncertainty (due to the inevitable residual frequency error) rendering them unsuitable for differential phase measurements used typically in satellite ranging and monopulse tracking systems which rely on differential phase measurements between two coherent signals processed by two downlink chains.

